



PATENT
Atty. Dkt. No. AMAT/5320.Y1/DSM/BCVD/JW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Eric W. Schieve

Patent No.: 6,793,766 B2

Issued: September 21, 2004

Serial No.: 09/755,516

Filed: January 4, 2001

For: APPARATUS HAVING
PLATFORMS POSITIONED
FOR PRECISE CENTERING
OF SEMICONDUCTOR WAFERS
DURING PROCESSING

Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

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Group Art Unit:

Examiner: Karla Moore

CERTIFICATE OF MAILING 37 CFR 1.8	
<p>I hereby certify that this correspondence is being deposited on <u>April 28</u>, 2005 with the United States Postal Service as First Class Mail in an envelope addressed to: Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.</p>	
<u>4/28/2005</u>	<u>George W. Holt</u>
Date	Signature

REQUEST FOR CERTIFICATE OF CORRECTION

Attached is a Certificate of Correction for correcting several errors in the printed patent.

Applicant submits that at least one of the errors mentioned above was made by the applicant. Therefore, please charge the fee of \$100.00 to deposit account no. 20-0782/AMAT/5302.Y1/BTP.

Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 6,793,766 B2

DATED : September 21, 2004

INVENTOR(S) : Eric W. Chieve, Lawrence Chung-Lai Lei

It is certified that error appears in the above-identified patent and that said Letters Patent
are hereby corrected as shown below:

Column 9, line 49, please delete "special" and insert -- spatial --.

PTO/SB/44 (04-04)
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